

**The 22nd IEEE Pacific Rim International Symposium on
Dependable Computing (PRDC 2017)**

22 – 25 January 2017, Christchurch, New Zealand

FINAL PROGRAM



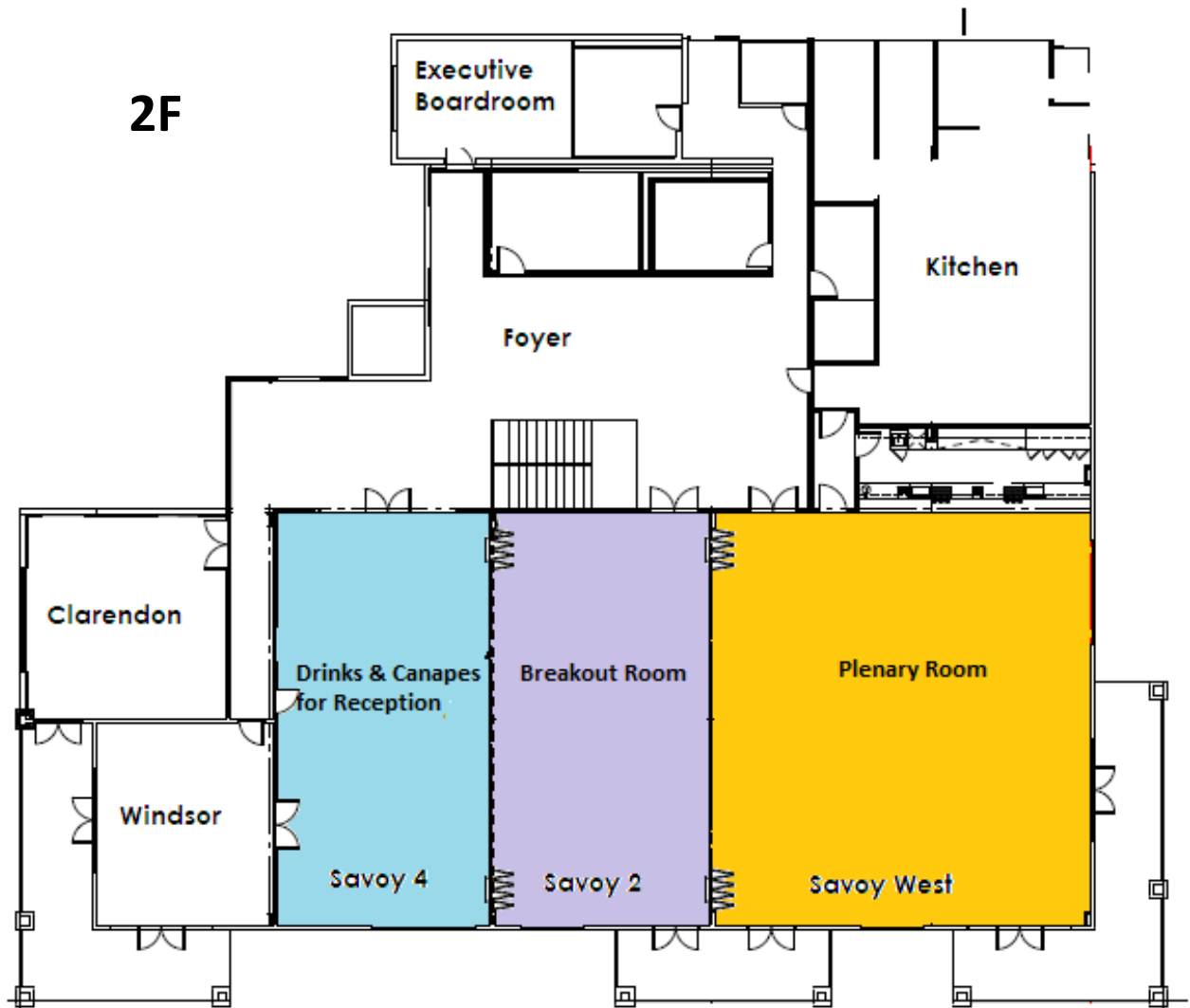
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Final Program at Glance

	Sun 22-Jan	
17:00 - 21:00	On-Site Registration	
19:00 - 21:00	Reception (Savoy 4)	
	Mon 23-Jan	
08:00 - 17:00	On-Site Registration	
09:00 - 09:30	Opening Ceremony (Savoy West)	
09:30 - 10:30	Keynote 1 (Savoy West)	
10:30 - 11:00	Coffee/Tea Break (Foyer)	
11:00 - 12:30	Session 1A: Dependable Network 1 (Savoy West)	Session 1B: Evaluation of Dependability (Savoy 2)
12:30 - 14:00	Lunch at Rydges Latimer Christchurch Hotel (downstairs buffet)	
14:00 - 15:30	Session 2A: Testing (Savoy West)	Session 2B: Industrial + Workshop 1 (Savoy 2)
15:30 - 16:00	Coffee/Tea Break (Foyer)	
16:00 - 17:00	Session 3A: Dependable Network 2 (Savoy West)	Session 3B: Dependable Systems (Savoy 2)
	Tue 24-Jan	
08:00 - 17:00	On-Site Registration	
08:30 - 09:30	Keynote 2 (Savoy West)	
09:30 - 11:00	Panel Discussion (Savoy West)	
11:00 - 11:30	Coffee/Tea Break (Foyer)	
11:30 - 12:30	Session 4A: Security 1 (Savoy West)	Session 4B: Smart Grid (Savoy 2)
12:30 - 14:00	Lunch at Rydges Latimer Christchurch Hotel (downstairs buffet)	
14:00 - 15:30	Session 5A: Dependable VLSI (Savoy West)	Session 5B: Fast Abstract and Posters (Savoy 2)
15:30 - 16:00	Coffee/Tea Break (Foyer)	
16:00 - 17:00	Session 6A: Security 2 (Savoy West)	Session 6B: Industrial + Workshop 2 (Savoy 2)
19:00 - 21:00	Banquet	
	Wed25-Jan	
08:00 - 11:00	On-Site Registration	
09:30 - 10:30	Session 7A: Dependability for Traffic Systems 1 (Savoy West)	Session 7B: Coding (Savoy 2)
10:30 - 11:00	Coffee/Tea Break (Foyer)	
11:00 - 12:00	Session 8A: Dependability for Traffic Systems 2 (Savoy West)	Session 8B: Security 3 (Savoy 2)

Map



Welcome Messages from PRDC 2017 General Chair

It is a great pleasure to welcome you to IEEE PRDC 2017, the 22nd edition of the IEEE Pacific Rim International Symposium on Dependable Computing. This year PRDC will be held in Christchurch, New Zealand. Christchurch is known internationally as the “Garden City” because of its spectacular gardens. It is my great pleasure and privilege to host PRDC in Christchurch, New Zealand.

We have an exciting program planned for the attendees this year. We have 30 papers in the regular research paper track, we have quality papers in other tracks such as the industry track, fast abstracts track, the posters along with the workshop.

First, I would like to express my special appreciation to Professor Takashi Nanya, the former PRDC Steering Committee Chair and Dr Yennun Huang, the current Steering Committee Chair of PRDC, and other members of the Steering Committee for their kind guidance and support.

Special thanks go to the Program co-chairs, Professor Vijay Varadharajan and Professor Masato Kitakami to prepare excellent conference program. I am very grateful to other chairs of the organizing committee including, Fast Abstract Chair: Paul R. M. Maciel; Industrial Track Chair: Fumio Machida; Poster Chair: Matthias Galster; Publicity Co-Chairs: Tatsuhiro Tsuchiya and Javier Alonso; and Finance Chair: Walter Guttmann. I would like to especially thank Jin Hong for his enormous help and support during all stages of the conference. I would like to express my gratitude to all the members of the technical program committee and the many volunteers who continuously supported the conference.

Finally, I am grateful to all of you for participating in PRDC 2017 and making PRDC a success. We look forward to seeing you in Christchurch, New Zealand!

PRDC 2017 General Chair

Dong Seong Kim, *University of Canterbury, New Zealand*

Welcome Messages from PRDC 2017 Program Chairs

We welcome you to the IEEE 22nd Pacific Rim International Symposium on Dependable Computing (PRDC 2017) in Christchurch, New Zealand.

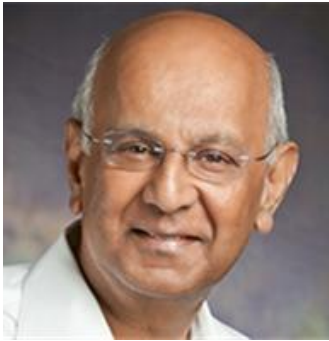
This year we had 53 submissions from 17 countries. We selected 30 regular papers through a rigorous reviewing process. Each paper has been reviewed by at least three reviewers. We are extremely grateful to all the members of the program committee and external reviewers for their care and diligence for quality reviews.

We further thank the two keynote speakers who have kindly accepted our invitations. The technical program also includes seven industry track papers, eight fast abstracts and four poster papers. PRDC 2017 would not be possible without the hard work of many people. We wish to express our gratitude to all those who took part in the symposium.

PRDC 2017 Program Chairs

Masato Kitakami, *Chiba University, Japan*

Vijay Varadharajan, *Macquarie University, Australia*



Keynote 1: Achieving Resilience in Newer Application Domains

Speaker: Prof. Ravishankar K. Iyer
University of Illinois at Urbana-Champaign, Illinois, USA

(Room: Savoy West, Time: Monday 23, 09:30 – 10:30)

About the Keynote Speaker

Bio: Ravishankar Iyer is the George and Ann Fisher Distinguished Professor of Engineering at the University of Illinois at Urbana-Champaign. He holds joint appointments in the Department of Electrical and Computer Engineering, the Coordinated Science Laboratory (CSL), and the Department of Computer Science and serves as Chief Scientist of the Information Trust Institute and is affiliate faculty of the National Center for Supercomputing Applications (NCSA). Iyer has led several large successful projects funded by the National Aeronautics and Space Administration (NASA), Defense Advanced Research Projects Agency (DARPA), National Science Foundation (NSF), and industry. He currently co-leads the CompGen Initiative at Illinois. Funded by NSF and partnering with industry leaders, hospitals, and research laboratories, CompGen aims to build a new computational platform to address both accuracy and performance issues for a range of genomics applications. Professor Iyer is a Fellow of the American Association for the Advancement of Science, the Institute of Electrical and Electronics Engineers (IEEE), and the Association for Computing Machinery (ACM). He has received several awards, including the American Institute for Aeronautics and Astronautics (AIAA) Information Systems Award, the IEEE Emanuel R. Piore Award, and the 2011 Outstanding Contributions award by the Association of Computing Machinery-Special Interest Group on Security for his fundamental and far-reaching contributions in secure and dependable computing. Professor Iyer is also the recipient of the degree of Doctor Honoris Causa from Toulouse Sabatier University in France.

Abstract: Above and beyond all the trends and developments in computer system design and implementation, are factors related to new applications domains. We see an eclectic landscape that presents compelling new research, business, and societal challenges-driven by the multidisciplinary nature of societal problems-at the nexus of food, health, and energy. The next major innovations in computer science and engineering are likely to come from "intelligent" deployment of human-centric systems that must optimally interact with other manmade and natural systems with a focus on seamless availability of dynamic decision-making capabilities. Our need to measure, model, communicate, and manage our vast natural and social enterprise requires a recognition that our physical world is complex and requires a level of adaptation and resilience that is unprecedented. New creative approaches are essential for handling the problems of scale and complexity envisioned in this future.

Applications, such as, in High Performance Computing, the smart power grid, medical electronics, robotic management and control, all driven by advanced data-analytics, have spawned a level of complexity where traditional fault-tolerance methods cannot form the backbone for resilient system design. Assumptions such as occurrence of a single fault in the system, independence of faults, or low hardware and software interactions are easily foiled in these new settings. This means that a new set of fault models have to evolve and need to be studied carefully, either by significantly enhancing traditional techniques or by introducing new resiliency methods. Driven by studies of and observations from current technologies and systems, this talk will present research directions and challenges related to building resilient applications in several of the new domains.



Keynote 2: Reliability and Availability Modeling in Practice

Speaker: Prof. Kishor S. Trivedi
Duke University, North Carolina, USA

(Room: Savoy West, Time: Tuesday 24, 08:30 – 09:30)

About the Keynote Speaker

Bio: Kishor S. Trivedi holds the Hudson Chair in the Department of Electrical and Computer Engineering at Duke University, Durham, NC. He has a B. Tech (EE, 1968) from IIT Mumbai, M.S. (CS, 1972) and PhD (CS, 1974) from the University of Illinois, Urbana-Champaign. He has been on the Duke faculty since 1975. He is currently a visiting professor at IIT Gandhinagar. He is the author of a well-known text entitled, Probability and Statistics with Reliability, Queuing and Computer Science Applications, first published by Prentice-Hall; a thoroughly revised second edition (including its Indian edition) of this book has been published by John Wiley. He is a Life Fellow of the Institute of Electrical and Electronics Engineers. He is a Golden Core Member of IEEE Computer Society. He has published over 500 articles and has supervised 46 Ph.D. dissertations. He is the recipient of IEEE Computer Society Technical Achievement Award for his research on Software Aging and Rejuvenation. His research interests are in reliability, availability, performance, performability and survivability modeling of computer and communication systems. He works closely with industry in carrying out reliability/availability analysis, providing short courses on reliability, availability, performability modeling and in the development and dissemination of software packages such as SHARPE and SPNP. His URL is www.ee.duke.edu/~ktrivedi.

Abstract: Non-state-space solution methods are often used to solve reliability block diagrams, fault trees and reliability graphs. Relatively efficient algorithms are known to handle systems with hundreds of components and have been implemented in many software packages. Nevertheless, many practical problems cannot be handled by such algorithms. Bounding algorithms are then used in such cases as was done for a major subsystem of Boeing 787. Non-state-space methods derive their efficiency from the independence assumption that is often violated in practice. State space methods based on Markov chains, stochastic Petri nets, semi-Markov and Markov regenerative processes can be used to capture various kinds of dependencies among system components. However, the resulting state space explosion severely restricts the size of the problem that can be solved. Hierarchical and fixed-point iterative methods provide a scalable alternative that combines the strengths of state space and non-state-space methods and have been extensively used to solve real-life problems. We will take a journey through these model types via interesting real-world examples.

Panel Discussion

(Room: Savoy West, Time: Tuesday 24, 09:30 – 11:00)

Panel Discussion Topic:

Security and Privacy of Big-data Applications

Panellists:

Dr. Yennun Huang

Prof. Neeraj Suri

Prof. William Sanders

Prof. Paulo Esteve-Verissimo

Prof. Armin Zimmermann

Sessions and Papers in PRDC 2017

Monday 23 January, 2016

Session 1A: Dependable Network 1

(Room: Savoy West, 11:00 ~ 12.30)

Chair: Sy-Yen Kuo, National Taiwan University

- 1) Voting in the Presence of Byzantine Faults
-L. Tseng
- 2) On Dependability, Cost and Security Trade-off in Cloud Data Centers
-S. K. Mondal, S. S. Abadhan and J. Muppala
- 3) Accelerating Integrity Verification on Secure Processors by Promissory Hash
-M. Miyanaga, H. Irie and S. Sakai

Session 1B: Evaluation of Dependability

(Room: Savoy 2, 11:00 ~ 12.30)

Chair: Satoshi Fukumoto, Tokyo Metropolitan University

- 1) Robust ORAM: Enhancing Availability, Confidentiality and Integrity
-T. P. Thao, A. Miyaji, M. S. Rahman, S. Kiyomoto and A. Kubota
- 2) Resilience Benchmarking of Transactional Systems: Experimental Study of Alternative Metrics
-R. Almeida, A. A. Neto and H. Madeira
- 3) Mercury: Performance and Dependability Evaluation of Systems with Exponential, Expolynomial and General Distributions
-P. Maciel, R. Matos, B. Silva, J. Figueiredo, D. Oliveira, I. Fé, R. Maciel and J. R. Dantas

This presentation has been moved to Session 6B

Session 2A: Testing

(Room: Savoy West, 14:00 ~ 15.30)

Chair: Toshinori Hosokawa, Nihon University

- 1) Detection of Recovery Patterns in Cluster System Using Resource Usage Data
-N. Gurumdimma and A. Jhumka

This will be a video presentation.

- 2) Light-Weight Techniques for Improving the Controllability and Efficiency of ISA-Level Fault Injection Tools
-B. Sangchoolie, R. Johansson and J. Karlsson
- 3) Generating High Strength Test Suites for Combinatorial Interaction Testing Using ZDD-Based Graph Algorithms
-T. Ohashi and T. Tsuchiya

Session 2B: Industrial + Workshop 1

(Room: Savoy 2, 14:00 ~ 15.30)

Chair: Thao Tran Phuong, KDDI Research, Inc.

- 1) Expolynomial Modelling for Supporting VANET Infrastructure Planning
-A. Lobo Jr, R. Matos, B. Silva and P. Maciel

This presentation has been moved to session 5B poster.

- 1) Increasing the Accuracy of Cost and Availability Predictions of Quorum Protocols
-R. Schadek and O. Theel
- 2) A Rejuvenation Strategy of Two-Granularity Software Based on Adaptive Control
-Y. Fang, B. B. Yin, G. Ning, Z. Zheng and K. Y. Cai
- 3) Meeting the Challenges of Critical and Extreme Dependability and Security - Analysis and Architectural Solutions
-P. E. Verissimo, M. Völp, J. Decouchant, V. Rahli and F. Rocha

Session 3A: Dependable Network 2

(Room: Savoy West, 16:00 ~ 17.00)

Chair: Lewis Tseng, University of Illinois

1) A Built-in Circuit for Self-Repairing Mesh-Connected Processor Arrays with Spares on Diagonal

-I. Takanami and M. Fukushi

2) Key Management in Internet of Things via Kronecker Product

-I. C. Tsai, C. M. Yu, H. Yokota and S. Y. Kuo

Session 3B: Dependable Systems

(Room: Savoy 2, 16:00 ~ 17.00)

Chair: Jin Hong, University of Canterbury

1) Soft Errors Susceptibility of Virtualization Servers

-F. Cerveira, R. Barbosa and H. Madeira

2) An Empirical Investigation of Fault Triggers in Android Operating System

-F. Qin, Z. Zheng, X. Li, Y. Qiao and K. S. Trivedi

Sessions and Papers in PRDC 2017

Tuesday 24 January, 2016

Session 4A: Security 1

(Room: Savoy West, 11:30 ~ 12.30)

Chair: Haruo Yokota, Tokyo Institute of Technology

- 1) Learning Process Behavioral Baselines for Anomaly Detection
-A. Fawaz and W. Sanders
- 2) Integration of Integrity Enforcing Technologies into Embedded Control Devices: Experiences and Evaluation
-T. Rauter, A. Höller, J. Iber and C. Kreiner

Session 4B: Smart Grid

(Room: Savoy 2, 11:30 ~ 12.30)

Chair: Masayuki Arai, Nihon University

- 1) Application of Congestion Notifications in a Cyber-Physical System
-S. Jackson and B. McMillin
- 2) SeReCP: A Secure and Reliable Communication Platform for the Smart Grid
-K. Demir and N. Suri

Session 5A: Dependable VLSI

(Room: Savoy West, 14:00 ~ 15.30)

Chair: Masaru Fukushi, Yamaguchi University

- 1) Designing Networks-on-Chip for High Assurance Real-Time Systems
-E. A. Rambo, C. Seitz, S. Saidi and R. Ernst
- 2) Multi-Site Synchronous VM Replication for Persistent Systems with Asymmetric Read/Write Latencies
-V. A. Sartakov and R. Kapitza

This will be a video presentation.

- 3) Attack Induced Common-Mode Failures on PLC-based Safety System in a Nuclear Power Plant

-Z. Kalbarczyk, B. Lim, D. Chen and R. Iyer

Session 5B: Fast Abstract and Posters

(Room: Savoy 2, 14:00 ~ 15.30)

Chair: Hyuk Lim, GIST

Fast Abstract:

- 1) Fast Controller-Switching for Fault-Tolerant Cyber-Physical Systems on Software-Defined Networks
-S. Yoon, J. Lee, Y. Kim, S. Kim, and H. Lim
- 2) Spare-Tile-Based Dependable Logic Design for Sea-of-Tiles Architecture with Ambipolar Devices
-D. Takahashi and M. Arai
- 3) Erasure-Code-Based DTN Multi-Path Routing for Contact Avoidance
-H. Arai and M. Arai
- 4) A Diagnostic Fault Simulation Method for a Single Universal Logical Fault Model
-T. Hosokawa, H. Takano, H. Yamazaki, and K. Yamazaki
- 5) Bitcoin's Consistency Property
-L. Tseng
- 6) A Hardware Trojan Circuit Detection Method Using Activation Sequence Generations
-M. Yoshimura, T. Bouyashiki, and T. Hosokawa
- 7) Efficient Fault-Tolerant Clock Synchronization in Ring Topologies
-K. Echtele and Z. Moztarzadeh
- 8) Towards Outsourced Privacy-preserving Multiparty DBSCAN
-M. S. Rahman, A. Basu, and S. Kiyomoto

Posters:

- 1) Instagram Spam Detection
-*W. Zhang and H. M. Sun*
- 2) Dependability Evaluation of AFDX Real-Time Avionic Communication Networks
-*A. Zimmermann and P. Maciel*
- 3) Smart Cage Implementation with Dependable Safety Agent for Dogs
-*K. S. Ng, P. Y. Chen and P. H. Ting*
- 4) Trend Analyses of Failures in Information Systems - A Case Study on Communications Networks and Financial Information Systems
-*K. Bando, Y. Matsuno, Y. Ishigaki and K. Tanaka*

Session 6A: Security 2

(Room: Savoy West, 16:00 ~ 17.00)

Chair: Ahmed Fawaz, UIUC

- 1) Comparison of Corrupted Sensor Data Detection Methods in Detecting Stealthy Attacks on Cyber-Physical Systems
-*G. Sabaliauskaite, G. S. Ng, J. Ruths and A. Mathur*
- 2) An Abstraction Model and a Comparative Analysis of Intel and ARM Hardware Isolation Mechanisms
-*G. Averlant, B. Morgan, E. Alata, V. Nicomette and M. Kaâniche*

Session 6B: Industrial + Workshop 2

(Room: Savoy 2, 16:00 ~ 17.00)

Chair: Mengmeng Ge, University of Canterbury

- 1) Mercury: Performance and Dependability Evaluation of Systems with Exponential, Exponential and General Distributions
-*P. Maciel, R. Matos, B. Silva, J. Figueiredo, D. Oliveira, I. Fé, R. Maciel and J. R. Dantas*
 - ~~1) Capacity Oriented Availability model for Resources Estimation on Private Cloud Infrastructure
-*C. Melo, R. Matos, J. Dantas and P. Maciel*~~
- This presentation has been moved to session 5B poster.**
- 2) Method and Case Study of Model Checking Concurrent Systems That use Unbounded Timestamps
-*S. Nakano and T. Tsuchiya*
 - 3) Formal Specification and Verification of Security Guidelines
-*Z. Zhioua, Y. Roudier and R. B. Ameer*

This will be a video presentation.

Sessions and Papers in PRDC 2017

Tuesday 24 January, 2016

Session 7A: Dependability for Traffic Systems 1

(Room: Savoy West, 09:30 ~ 10.30)

Chair: Tatsuhiro Tsuchiya, Osaka University

- 1) On Train Automatic Stop Control Using Balises: Attacks and a Software-Only Countermeasure
-*W. G. Temple, B. A. N. Tran, B. Chen, Z. Kalbarczyk and W. H. Sanders*
- 2) Electromagnetic Noise Tolerant Hybrid Communication Protocol for CANs
-*M. Nakamura, K. Konomi, M. Ohara, K. Sakai and S. Fukumoto*

Session 7B: Coding

(Room: Savoy 2, 09:30 ~ 10.30)

Chair: Masato Kitakami, Chiba University

- 1) MEC: Network Optimized Multi-stage Erasure Coding for Scalable Storage Systems
-*H. Akutsu, T. Yamamoto, K. Ueda and H. Saito*
- 2) A Novel Highly Available Data Replication Strategy exploiting Data Semantics, Coding Techniques and Prior At-Hand Knowledge
-*A. Usman, R. Schadek and O. Theel*

Session 8A: Dependability for Traffic Systems 2

(Room: Savoy West, 11:00 ~ 12:00)

Chair: William Temple, ADSC Illinois at Singapore

- 1) A Probabilistic Analysis of a Leader Election Protocol for Virtual Traffic Lights
-*N. Fathollahnejad, R. Barbosa and H. Karlsson*
- 2) Specification, Implementation and Verification of Dynamic Group Membership for Vehicle Coordination
-*M. Asplund, J. Lövhall and E. Villani*

Session 8B: Security 3

(Room: Savoy 2, 11:00 ~ 12:00)

Chair: Jin Hong, University of Canterbury

- 1) Accounting for the Human User in Predictive Security Models
-*M. A. Nouredine, A. Marturano, K. Keefe, M. Bashir and W. H. Sanders*
- 2) Evaluating the Risk of Data Disclosure Using Noise Estimation for Differential Privacy
-*H. L. Chen, J. Y. Chen, Y. T. Tsou, C. M. Yu, B. C. Tai, S. C. Li, Y. Huang and C. M. Li*

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