

Saturday, November 17, 2012		
Time	Event	
17:00-20:00	Reception & Registration (Venue: Hotel Nikko Niigata, 3rd floor, Kujaku)	
Sunday, November 18, 2012		
Time	Event	
8:00-17:00	Registration (2nd floor foyer, outside Room 201B)	
8:30-9:00	Opening (Snow Hall B)	
9:00-10:00	Keynote I Chair: K. Iwasaki Takashi Nanya (Canon Inc., Japan) Toward Dependability of Information Society	
10:00-10:30	Break	
10:30-11:40	Regular Session (Component-based Systems: Room 201A) Chair: T. Tsuchiya Miruna Stoicescu, Jean-Charles Fabre and Matthieu Roy From design for adaptation to component-based resilient computing Qiang Guan, Chi-Chen Chiu and Song Fu CDA: A cloud dependability analysis framework for characterizing system dependability in cloud computing infrastructures	Industry Session (Security: Room 201B) Chair: M. Tsunoyama Nobuyasu Kanekawa IEC61508: Are there royal roads to SIL4 with on-chip redundancy? Susumu Kobayashi and Fumihito Minami A clock skew minimization method for tolerance to interconnect process variations
	11:40-12:00	Kumiko Tadano, Fumio Machida, Jianwen Xiang and Yoshiharu Maeno Identification of minimal unacceptable combinations of simultaneous component failures in information systems Fast Abstract I (Room 201B) Chair: H. Okamura S. Gu, Y. Wu and S. Xu, A Verification of Conjecture on Distance Effect in Pseudo-Random Testing C. Tong, S. Xu and Y. Wu, Third-Order Syndrome Testing
12:00-13:30	Lunch	
13:30-15:00	Regular Session (Security: Room 201A) Chair: T. Kikuno Naghmeh Ivaki, Filipe Araujo and Raul Barbosa A middleware for exactly-once semantics in request-response interactions Kenichi Kourai and Takuya Nagata A secure framework for monitoring operating systems using SPEs in Cell/B.E. Linbo Chen, Jianhui Jiang and Danqing Zhang Code reuse prevention through control flow lazily check	Fast Abstract II (Room 201B) Chair: H. Okamura I. Lopatkin, A. Iliasov and A. Romanovsky, Rigorous Step-Wise Development of Fault Tolerance S. Tanaka, S. Kurosawa and H. Kobayashi, Server Side Assertion Using SQL/Trigger from UML/OCL in Software Development R. Yanase, T. Sakai, M. Sakai and S. Yamane, A New Approach to Specify and Verify Embedded Systems consisting of CPU and DRP J. Xiang, F. Machida, K. Tadano, K. Yanoo and Y. Maeno, Isolation of Irrelevant Components in Systems with Imperfect Coverage Y. Miwa, T. Yokogawa, S. Amasaki, R. Kawamura, Y. Sato and H. Miyazaki, A Method for Translating Timed Petri Nets into Compact Timed Automata Thanh-Trung Pham, Quyet-Thang Huynh and X. Defago, Making Reliability Modeling of Component-based Systems Usable in Practice Thanh Dang Nguyen, F. Bonnet and X. Defago, Byzantine Fault Tolerant Protocols for Task Management in Mobile Robots D. Higashihara and X. Defago, Reliable Micro-protocols Composition and Combination
	15:00-15:30	Break
15:30-17:00	Regular Session (Software Systems: Room 201A) Chair: S.-Y. Kuo Amina Mekki Mokhtar, Jean-Paul Blanquart, Jeremie Guiochet, Matthieu Roy and David Powell Safety trigger conditions for critical autonomous systems Salvador Decelles and Nagarajan Kandasamy Entropy-based detection of incipient faults in software systems Tatsuya Noguchi, Tatsuhiro Tsuchiya and Tohru Kikuno Safety verification of asynchronous consensus algorithms with model checking	Workshop I: (WDTs: Room 201B) Chair: M. Ohara (Invited Talk) Korefumi Tashiro (National Traffic Safety and Environment Laboratory) Trend and issues of international standards for a railway safety control Alexei Iliasov and Alexander Romanovsky SAFECAP domain language for reasoning about safety and capacity Fumihito Higuchi and Masaya Mori Next generation OS for railway signalling safety devices - development of exclusive double thread OS (EDTOS) -
	17:00-18:00	Embedded Tutorial (Room 201A) Chair: T. Yoneda Ken Takeuchi (Chuo University, Japan) Highly Reliable Signal Processing Technologies for Dependable Solid-State Drives (SSDs) Guo Xie, Xinhong Hei, Hiroshi Mochizuki, Takahashi and Hideo Nakamura Formalizing and analyzing the train-to-wayside network system for CBTC
19:00-21:00	Banquet (Venue: Hotel Nikko Niigata, 30th floor, Banquet Hoh-oh)	

Monday, November 19, 2012

Time	Event	
8:00-17:00	Registration (2nd floor foyer, outside 201B)	
8:30-10:00	<p>Fast Abstract III (Room 201A) Chair: H. Irie</p> <p>A. Sugiki and K. Kato, Towards Robust Tuning of Hadoop by using Design of Experiments Approach</p> <p>M. Tsunoyama, Y. Imai, H. Hori, H. Jinno, M. Ogawa and T. Sato, Applying Fuzzy Measures and Integrals to Fault Diagnosis, Utilizing the Knowledge of Skilled Engineers</p> <p>A. Thomas and K. Pattabiraman, LLFI: An Intermediate-Code Level Fault Injection Tool</p> <p>Y. Shimizu, K. Sakurai and S. Yamane, Trace-mining Profile for Dependable Large-Scale Distributed Framework Hadoop</p> <p>S. Kotaki and M. Kitakami, Neighborhood Level Error Control Codes for Multilevel Systems</p> <p>M. Kitakami and D. Mamada, Data Movement Algorithm for Flash SSD with Long Lifetime</p> <p>T. Takagi, Z. Furukawa and K. Akiyama, Ralph's Chart Analysis: Test Analysis Technique Using Common Factors among Software Functions</p> <p>M. Ohara and S. Fukumoto, A Note on Dynamic Topology Management for Distributed Data Replication System with File Versioning</p> <p>C. Luo, H. Okamura and T. Dohi, Analysis of Fairness in Distributed Database System with Snapshot Isolation</p>	<p>Workshop II: Workshop on Recent Advances in Software Dependability (1) (RASD2012: Room 201B) Chair: M. Kimura</p> <p>Shinji Inoue and Shigeru Yamada A bootstrap method for software reliability assessment based on a discrete NHPP model</p> <p>Mitsuhiro Kimura and Ryohei Shimada A note on static software reliability models by GMDH: comparison with a multiple regression model</p> <p>Xufeng Zhao, Syouji Nakamura, and Toshio Nakagawa Optimal inspection first and last policies for a computer system</p>
10:00-10:30	Break	
10:30-12:00	<p>Regular Session (Hardware Reliability: Room 201A) Chair: K. Pattabiraman</p> <p>Ken Yano, Takanori Hayashida and Toshinori Sato Analysis of SER Improvement by Radiation Hardened Latches</p> <p>Itsuo Takanami and Tadayoshi Horita A built-in circuit for self-repairing mesh-connected processor arrays by direct spare replacement</p> <p>Tomohiro Yoneda, Masashi Imai, Naoya Onizawa Atsushi Matsumoto and Takahiro Hanyu. Multi-chip NoCs for automotive applications (Practical Experience Report)</p>	<p>Workshop II: Workshop on Recent Advances in Software Dependability (2) (RASD2012: Room 201B) Chair: M. Kimura</p> <p>Invited Talk (60min) Shigeru Nishiyama (Niigata University of International and Information Studies, Japan) Function Point Analysis: Its "functions" for and effects on software development processes</p> <p>Hiroyuki Okamura, Masataka Tokuzane, and Tadashi Dohi Security evaluation for software system with vulnerability life cycle and user profiles</p>
12:00-13:30	Lunch	
13:30-14:30	<p>Keynote II (4th floor, Marine Hall) Chair: T. Dohi Krishnendu Chakrabarty (Duke University, USA) Test and DfT as Enablers for Dependable 3D Integrated Circuits</p>	
14:30-15:00	Break	
15:00-16:00	<p>Regular Session (Networking: Room 201A) Chair: X. Defago</p> <p>Masashi Imai and Tomohiro Yoneda Performance modeling and analysis of on-chip networks for real-time applications</p> <p>Tatsuhiko Tsuchiya A BDD-based approach to reliability optimal module allocation in networks</p>	<p>Workshop II: Workshop on Recent Advances in Software Dependability (3) (RASD2012: Room 201B) Chair: S. Fukumoto</p> <p>Mitsutaka Kimura, Mitsuhiro Imaizumi, and Toshio Nakagawa Optimal policy of a server system with replication buffering relay method</p> <p>Shunsuke Tokumoto, Tadashi Dohi, and Wong Young Yun Towards development of risk-based checkpointing scheme via parametric bootstrapping</p>
16:00-17:00	<p>Regular Session (Fault Diagnosis: Room 201A) Chair: M. Arai</p> <p>Aromhack Saysanasongkham, Masahiko Negishi, Masayuki Arai and Satoshi Fukumoto A dependable processor by using built in self test to tolerate periodical transient faults under highly electromagnetic environment</p> <p>Yoshinobu Higami, Hiroshi Takahashi, Shinya Kobayashi and Kewal Saluja Diagnosis for bridging faults on clock lines</p>	
17:00-17:15	Closing (Room 201A)	